RECEIVED
CENTRAL FAX CENTER
OCT 0 5 2006

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph 0056 of the published application with the following rewritten paragraph:

At setup of system 10 system manager 54 assigns a range of LBAs to each cache node 20. Manager 54 may subsequently reassign the ranges during operation of system, and an example of steps to be taken in the event of a node change is described below with reference to FIG. [[5]] 6. The ranges are chosen so that the complete memory address space of disks 12 is covered, and so that each LBA is mapped to at least one cache node; typically more than one is used for redundancy purposes. The LBAs are preferably grouped by an internal unit termed a "track," which is a group of sequential LBAs, and which is described in more detail below. The assigned ranges for each cache node 20 are preferably stored in each interface node 26 as a substantially similar table, and the table is used by the interface nodes in routing IO requests from hosts 52 to the cache nodes. Alternatively or additionally, the assigned ranges for each cache node 20 are stored in each interface node 26 as a substantially similar function, or by any other suitable method known in the art for generating a correspondence between ranges and cache nodes. Hereinbelow, the correspondence between cache nodes and ranges, in terms of tracks, is referred to as track-cache node mapping 28, and it will be understood that mapping 28 gives each interface node 26 a general overview of the complete cache address space of system 10.